

4-IDS  
8/19/12  
XA-9611

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hiroshi OKABE et al.

Appln. No.:

Filed: Herewith

For: ELECTRONIC CIRCUIT EQUIPMENT USING MULTILAYER  
CIRCUIT BOARD



\* \* \*

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion  
as to materiality or prior art effect, the document listed  
on the attached Form PTO-1449 is hereby cited.

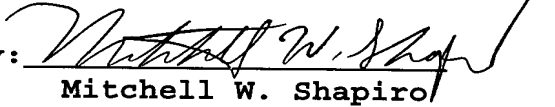
The document on the attached List is cited in the  
specification, on pages 1 and 3, and its relevance is  
indicated therein.

Respectfully submitted,

MWS:jab

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By:

  
Mitchell W. Shapiro  
Reg. No. 31,568

January 15, 2002